

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

In re PATENT APPLICATION of

Applicant(s) : Masaru TAKAISHI et al.

Appln. No. : To Be Assigned

Filed : December 31, 2003

For : SEMICONDUCTOR DEVICE

Atty. Dkt. : AI 321

**INFORMATION
DISCLOSURE
STATEMENT**

Commissioner for Patents
Washington, D.C. 20231

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b) (1).

Attached is a copy of one Japanese patent publication. Any relevance of Japanese publication can be gleaned from the attached English-language Abstract, and its discussion on page 1 of the specification. The document is listed on the attached Form PTO-1449.

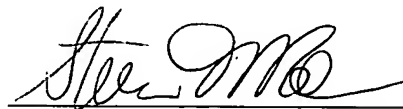
Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted document is respectfully requested.

Respectfully submitted,

December 31, 2003

Date

SMR:tl



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				Applicant Masaru TAKAISHI et al.					
				Filing Date December 31, 2003				Group Unit To Be Assigned	
U.S. PATENT DOCUMENTS									
Examiner Initial		Document Number	Date	Name	Class	Sub- Class	Filing Date		
	AA								
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		Document Number	Date	Country	Class	Sub- Class	Trans- lation		
	AL	61-179589	08/12/86	Japan			Abstract		
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.									